

**Schedule – summer semester 2018**

**Electronic Devices Laboratory Exercises**

**(TUE 12:00-14:15, TUE 17:45-20:00, FRI 8:00-10:15)**

26.02 – 4.03	-----
5.03 – 11.03	-----
12.03 – 18.03	Introduction to NI ELVIS II environment Exercise 0 – Investigation of RLC components
19.03 – 25.03	Exercise 1 – RC circuits
26.03 – 1.04	
2.04 – 8.04	
9.04 – 15.04	Exercise 2 – Semiconductor diodes
16.04 – 22.04	Exercise 3 – Applications of diodes
23.04 – 29.04	Exercise 4 – Junction Field Effect Transistor (JFET)
30.04 – 6.05	
7.05 – 13.05	Exercise 5 – Bipolar Junction Transistor (BJT)
14.05 – 20.05	Exercise 6 – Small signal parameters of the BJT
21.05 -27.05	
28.05 – 3.06	Exercise 7 – Switching the diodes and transistors
4.06 – 10.06	
11.06 – 17.06	Exercise 8 – MOS transistor
18.06 – 24.06	Final classes (45 min)

Web site <http://scalak.elektro.agh.edu.pl>