

**Schedule – summer semester 2019****Electronic Devices Laboratory Exercises (28H)****(TUE 8:00-10:15, TUE 10:15-12:30)**

25.02 – 3.03	-----
4.03 – 10.03	-----
11.03 – 17.03	Introduction to NI ELVIS II environment Exercise 0 – Investigation of RLC components
18.03 – 24.03	Exercise 1 – RC circuits
25.03 – 31.03	Exercise 2 – Semiconductor diodes
1.04 – 7.04	Exercise 3 – Applications of diodes
8.04 – 14.04	-----
15.04 – 21.04	
22.04 – 28.04	-----
29.04 – 5.05	-----
6.05 – 12.05	Exercise 4 – Junction Field Effect Transistor (JFET)
13.05 – 19.05	Exercise 5 – Bipolar Junction Transistor (BJT)
20.05 -26.05	Exercise 6 – Small signal parameters of the BJT
27.05 – 2.06	Exercise 7 – MOS transistor
3.06 – 9.06	Exercise 8 – Switching the diodes and transistors
10.06 – 16.06	Final classes (45 min)

Web site <http://scalak.elektro.agh.edu.pl>