

Schedule – summer semester 2017

Electronic Devices Laboratory Exercises

(Tue 5:45pm – 8:00pm, Wed 5:45pm – 8:00pm)

27.02 – 5.03	-----
6.03 – 12.03	Introduction to NI ELVIS II environment Exercise 0 – Investigation of RLC components
13.03 – 19.03	Exercise 1 – RC circuits
20.03 – 26.03	Exercise 2 – Semiconductor diodes
27.03 – 2.04	-----
3.04 – 9.04	Exercise 3 – Applications of diodes
<b>10.04 – 23.04</b>	Exercise 4 – Junction Field Effect Transistor (JFET)
24.04 – 30.04	-----
8.05 – 14.05	Exercise 5 – Bipolar Junction Transistor (BJT)
15.05 – 21.05	Exercise 6 – Small signal parameters of the BJT
22.05 -28.05	-----
29.05 – 4.06	Exercise 7 – MOS transistor
5.06 – 11.06	Final classes, credits. or/and: Exercise 8 – Switching the diodes and transistors
12.06 – 16.06	Final classes (45 min)

Web site <http://scalak.elektro.agh.edu.pl>