

**Schedule – summer semester 2020****Electronic Devices Laboratory Exercises (28H)****(TUE 10:00-12:15)**

24.02 – 1.03	-----
2.03 – 8.03	-----
9.03 – 15.03	Introduction to NI ELVIS II environment Exercise 0 – Investigation of RLC components
16.03 – 22.03	Exercise 1 – RC circuits
23.03 – 29.03	Exercise 2 – Semiconductor diodes
30.03 – 5.04	-----
6.04 – 12.04	Exercise 3 – Applications of diodes
13.04 – 19.04	
20.04 – 26.04	-----
27.04 – 3.05	Exercise 4 – Junction Field Effect Transistor (JFET)
4.05 – 10.05	Exercise 5 – Bipolar Junction Transistor (BJT)
11.05 – 17.05	-----
18.05 -24.05	Exercise 6 – Small signal parameters of the BJT
25.05 – 31.05	Exercise 7 – MOS transistor
1.06 – 7.06	Exercise 8 – Switching the diodes and transistors
8.06 – 14.06	Final classes (45 min)

Web site <http://scalak.elektro.agh.edu.pl>